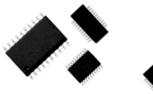


COMPLIANT

# 25 or 50 Mil Pitch, T-Filter Resistor/Capacitor Networks



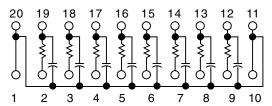


Small Outline, Surface Mount, EMI/RFI Reduction, T-Filter Networks

Vishay Thin Film's schematic AD is designed as an 8 channel filter for use with personal computer and peripheral 110 ports such as SCSI ports. The use of single die technology for filtering minimizes space and allows for more freedom in routing. With a rugged molded case to protect the circuit from the environment and an integrated thin film network this product is your choice when reduced size, improved accuracy and surface mount capability are your goals.

Available packages SOIC, SSOP and TSSOP.

#### **SCHEMATIC AD**



### **FEATURES**

- Lead (Pb)-free available
- · Resistors and capacitors on a single chip
- Saves board space
- · Reduces total assembly costs
- Uniform performance characteristics
- · Compatible with automatic surface mounting equipment
- UL 94V-0 flame resistant
- Rugged, molded case construction

### **TYPICAL PERFORMANCE**

	TCR	TOLERANCE
RESISTOR	200	10 %
	тсс	TOLERANCE
CAPACITOR	200	20 %

	MODELS		STANDAR	D VALUES
VSORC	VSSRC	VTSRC	R (Ohms)	C (pF)
	Х		33	47

STANDARD ELECTRICAL SPECIFICATIONS					
TEST MATERIAL		SPECIFICATIONS	CONDITION		
		TANTALUM NITRIDE ON SILICON			
Resistance Rang	е	10 Ohms to 750 Ohms			
Tracking		± 10 ppm/°C			
TCR:	Absolute	± 200 ppm/°C	0 °C to + 70 °C		
Toloropes	Absolute	± 10 % Standard (R)			
Tolerance:	Absolute	± 20 % Standard (C)	at 1 MHz & V <sub>RMS</sub> over + 10 °C to + 70 °C		
Power Rating:	Package	1 W - (T)SSOP. 1.2 W - SOIC	See Derating Curve		
Capacitance Range		10 pF to 150 pF - TSSOP/10 pF to 250 pF - SOIC and SSOP			
Stability: △R Ratio		± 2 %	1000 hrs.		
ESD Protection		> 2 kV	MIL-STD-883, Method 3015		
Breakdown Voltage		35 - 50 V			
Operating Temperature Range		0 °C to + 70 °C			
Storage Temperature Range		- 55 °C to + 125 °C			
Power Rating/Resistor		100 mW			

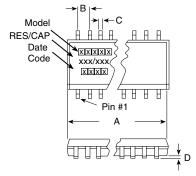
<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply

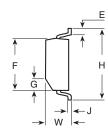


25 or 50 Mil Pitch, T-Filter Resistor/Capacitor Networks

Vishay Thin Film

### **DIMENSIONS AND IMPRINTING** in inches and millimeters

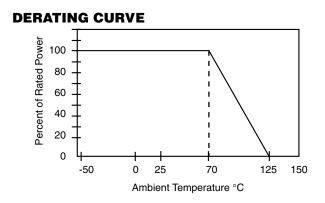




MODEL	VTSRC	VTSRC20-AD		VSSRC20-AD		VSORC20-AD	
	INCHES	MILLIMETERS	INCHES	MILLIMETERS	INCHES	MILLIMETERS	
Α	0.256 ± 0.003	$6.5 \pm 0.08$	0.344 Max.	8.74 Max.	0.500 ± 0.010	12.7 ± 0.25	
B (Ref.)	0.025	0.65	0.025	0.64	0.050	1.27	
C (Ref.)	0.0087	0.22	0.010	0.25	0.016	0.41	
D	0.004	0.10	0.006	0.15	0.008	0.20	
E (Typ.)	0.024	0.61	0.025	0.64	0.030	0.76	
F	0.173 ± 0.003	4.39 ± 0.08	0.154 ± 0.003	3.9	0.293 ± 0.003	7.44	
G	0.015 × 45°	0.38	0.015 × 45°	0.38	0.025 × 45°	0.64	
Н	0.252 ± 0.005	6.4 ± 0.13	0.236 ± 0.008	$6.0 \pm 0.20$	0.406 ± 0.005	10.31	
J (Ref.)	0.005	0.13	0.010	0.25	0.010	0.25	
W	0.043 ± 0.005	1.09 ± 0.13	0.064 ± 0.005	1.6	0.100 ± 0.005	2.59	

IMPRINTING				
VSORC, VSSRC, VTSRC	20	AD	xxx / xxx	
MODEL	PIN COUNT	SCHEMATIC	RESISTANCE / CAPACITANCE   Code: e.g. Code: e.g.   100 = 10 ohm 101 = 100 pF	
		XXXX Date Code	* Optional marking	

MECHANICAL SPECIFICATIONS			
Resistive Element	Tantalum Nitride		
Substrate Material	Silicon		
Body	Molded Epoxy		
Terminals	Copper Alloy		
Plating	Tin Lead		
Lead Coplanarity	0.0005 Inches		
Marking Resistance to Solvents	Permanency testing per MIL-STD-202, Method 215		
Lead (Pb)-free Option	100 % Sn Matte		
Lead (Pb)-free Finish	Plated		



PACKING INFORMATION				
MODEL	LEADS	TAPE AND REEL	TUBES	
VTSRC (TSSOP)	20	2500	74	
VSSRC (SSOP)	20	2500	55	
VSORC (SOIC)	20	1000	38	

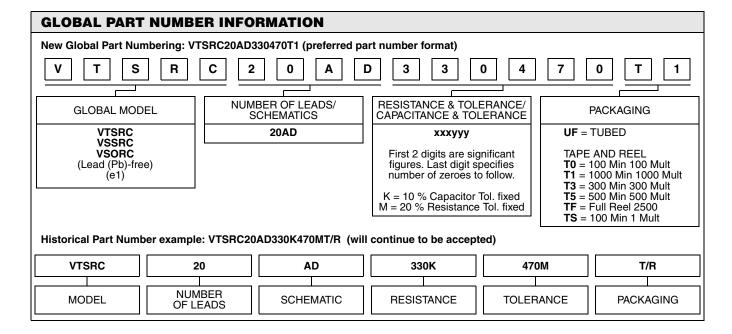
Document Number: 60086 Revision: 10-May-06

## VTSRC, VSSRC, VSORC-AD

Vishay Thin Film

25 or 50 Mil Pitch, T-Filter Resistor/Capacitor Networks





Document Number: 60086 Revision: 10-May-06

## **Legal Disclaimer Notice**



Vishay

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